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## Specification of FUJITSU TFT-LCD module

# FLC51UXC8V-11L

	Approval
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Ву :	

This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, Fujitsu shall not be liable against the Customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

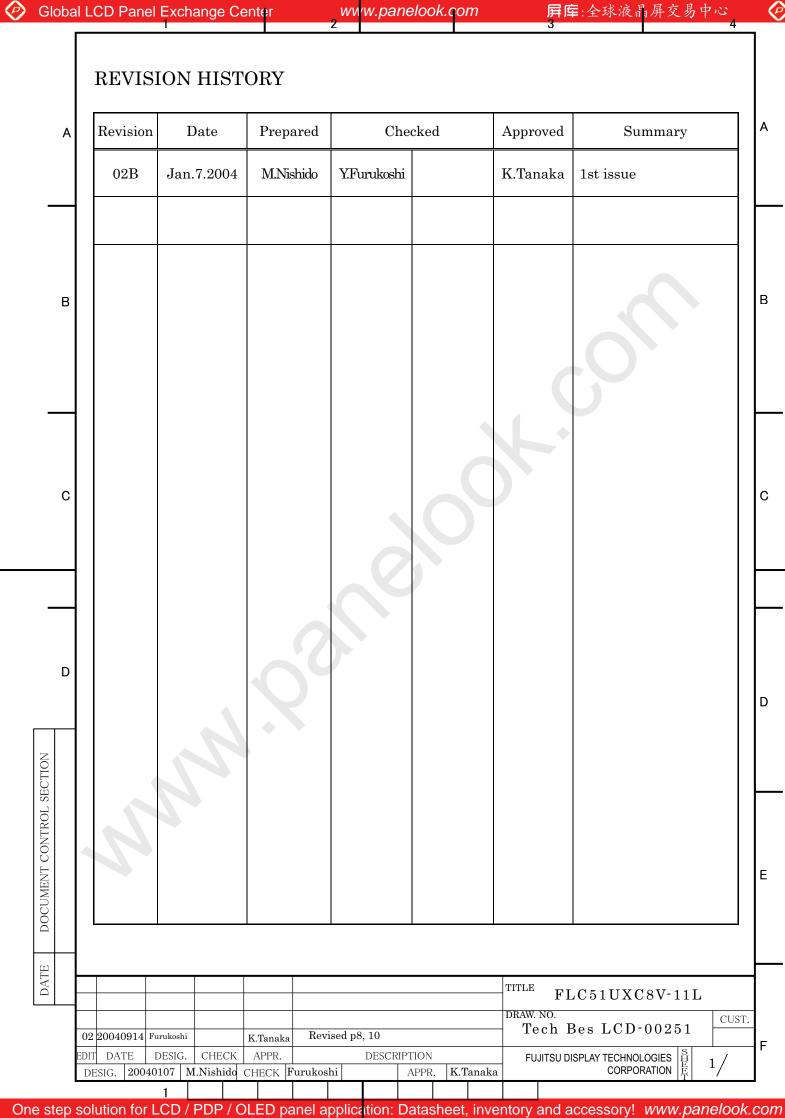
Specification No.: Tech Bes LCD-00251

Issue Date : September 14, 2004

Issued by: Makana Maraka

Katsunori. Tanaka Project Director LCD Products Div.

FUJITSU DISPLAY TECHNOLOGIES CORPORATION



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## 6. ABSOLUTE MAXIMUM RATING

Table 6-1 shows the absolute maximum rating of this LCD module.

Table 6-1 Absolute Maximum Rating

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vcc	Ta=25°C	-0.3	_	14.0	v
Input Signal Voltage (LVDS signal, PD, SEL LVDS,HRCOFF)	V <sub>IN</sub>	Ta=25°C	-0.3	-	3.6	V

#### 7. RECOMMENDED OPERATING CONDITIONS

Table 7-1 shows the recommended operating conditions of this LCD module.

Table 7-1 Recommended Operating Conditions

Item		Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage (Logic)		Vcc	11.5	12.0	12.5	V
Ripple Voltage	Vcc	$V_{ m RP}$	_	_	0.1	V

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#### 8. ELECTRICAL SPECIFICATIONS

Table 9 1 Floatrical Creations

Table 8-1 shows the electrical specifications of this LCD module. Figure 8-1 shows the measurement circuit. Figure 8-2(A) shows the equivalent circuit of the logic signal input area. Figure 8-2(B) shows the equivalent circuit of the supply voltage Input area.

<u>Table 8-1 Electrical S</u>	<u>Specifications</u>					.Г			
Item	Symbol	C	ondition	MIN.	TYP.	MAX.	Unit	Remark	
Differential-input Voltage (High)	V <sub>IH</sub>		V <sub>CM</sub> =+1.2V	_	_	100	mV		
Differential-input Voltage (Low)	V <sub>IL</sub>		V CM=+1.2 V	-100	_	-	mV		
Supply Current	Icc	V <sub>CC</sub> =+	-12.0±0.5V	_	600	1200	mA	*1	
Supply Rush Curre	nt I <sub>SCC</sub>		V K=81MHz	_	-	5.8	A	*2	
Supply Rush Currer Duration (1A excess		60Hz			-	0.2.	ms	۵	
B CCFL Turn on	Vs	f <sub>L</sub> =50l Ta=25			1600	1800	Vrms	*4	
A Voltage	VS	f <sub>L</sub> =50l Ta=0º			_	1850	Vrms	*4	
Lighting Voltage	ge V <sub>L</sub>		$\begin{array}{c} f_L = 50 kHz \\ I_L = 6 mA \end{array}$		800	_	Vrms		
G H Lighting Frequ	ency f <sub>L</sub>	V <sub>L</sub> =800Vrms		30	50	60	KHz		
Tube Current	IL	f <sub>L</sub> =501 V <sub>L</sub> =80	kHz 00Vrms	3	6	7	mArms	*3	

- (\*1) Typical current situation : Color bar pattern. Vcc=12.0VMaximum current situation: 2-pixel checker pattern. Vcc=11.5V Without rush current.
- (\*2) These items prescribe the rush current for starting internal DC/DC. Charging current to capacitors of Vcc is not prescribed.
- (\*3) Tube current ( $I_{\rm I}$ ) shows the value of the current that is consumed at one lamp. This LCD module has 6 lamps. Each 3 lamps are placed at upper side and lower side of the display.
  - 3 lamps are connected in parallel. Each low voltage terminals are connected with separate Cable to Back-light connecter.
- (\*4) The voltage above this value should be applied to the lamp for more than 1 second to startup. Otherwise the lamp may not be turned on.

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#### 9. OPTICAL SPECIFICATIONS

Table 9-1 shows the optical specifications of this LCD module.

Table 9-1 Optical Specifications

Ta=25°C,Signal timing=Typ.

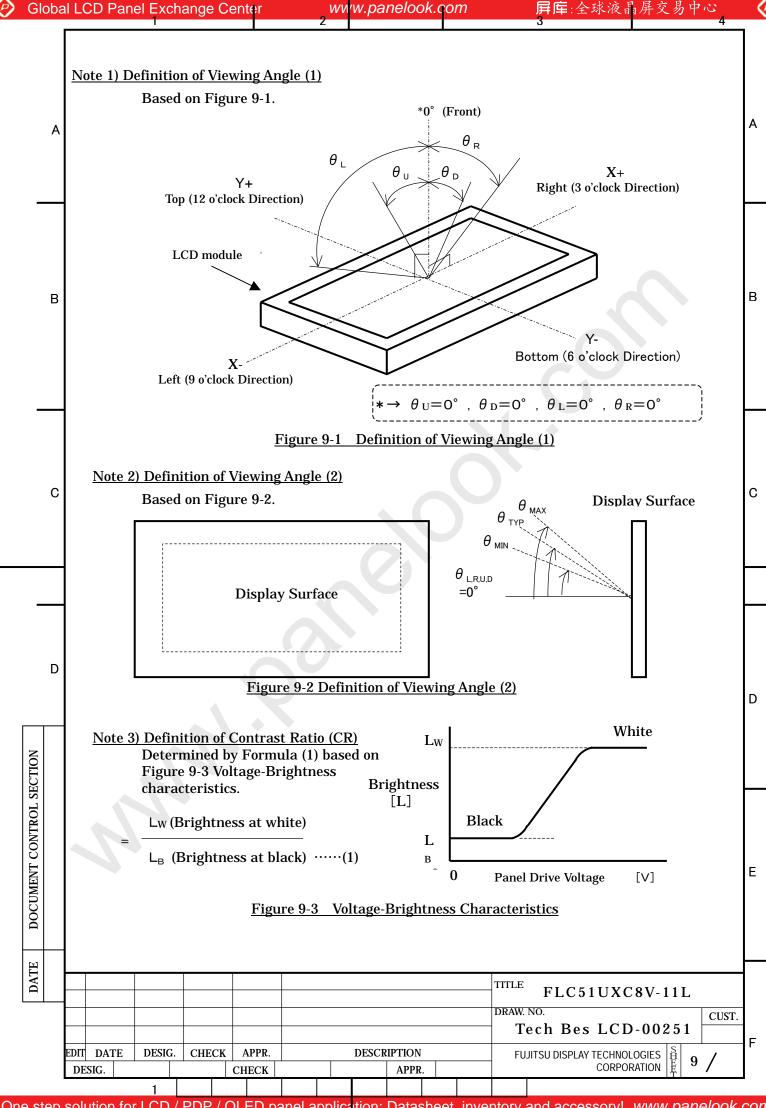
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	Item		Symbol	Cor	ndition	MIN.	TYP.	MAX.	Unit		Note
Visual	Horizonta	ıl	$\theta_{\mathrm{L,R}}$		$\theta_{\mathrm{U,D}}=0^{\circ}$	85	_	_	deg		(1)(2)
Angle	Vertical		$\theta$ U, D	CR≧10	$\theta_{\rm L,R}=0^\circ$	85			deg		(3)(5) (6)
	All Direct	ion	θ				80		deg		
Contrast	t Ratio		CR	$\theta_{ m L,R,U,I}$	) =0°	400	600	—	_	White/ Black	(1)(2) (3)(5)
②Respo	nse Rise+Fall)	•	<b>T</b> rise +	θ <sub>L, R,</sub>	Ta=25°C	_	16	_	ms		(1) (4)
(B→W-			<b>T</b> fall	=0°	Ta=0°C		34		ms		(5)
Respons Time (Ri (All gray	ise or Fall	1)	<b>T</b> avg	θ <sub>L, R,</sub> U, D  =0°	Ta=25°C 50-60Hz	_	210		ms	Average of Response Time	
Brightne	ess		I	θ <sub>L, R, U, I</sub>		200	250		cd/m <sup>2</sup>		(1)(5)
Brightne Uniform			ΔI	V <sub>CC</sub> =12. I <sub>L</sub> =6m f <sub>l</sub> =50k	A	70		_	%	White	(1)(5) (7)
		w	X	R*,G*,B =All"H"		0.283	0.313	0.343		"1	
		VV	У	–Ап п		0.299	0.329	0.359	_		(4)
Chroma	ticity	R			Red		(0.641,0.3	50) Typ.			(1) (5)
		G	(x, y)		Green	(	(0.287,0.5	95) Typ.			
		В			Blue	(	(0.142,0.0	70) Typ.			
LCD Par	nel Type					TFT Col	lor				
Display	Mode				7	Normall	ly Black				
Wide Vie	ewing Ang	gle Te	chnology			MVA					
Optimu	n Viewing	g Ang	le			_	(Syn	nmetry)			(6)
Display	Color					16,777,2	216 (8-bi				
Color of	non-displ	ay ar	ea			Black					
Surface	Treatmen	ıt				Anti-gla (Haze va	re alue:(25%	(a), 2H)			

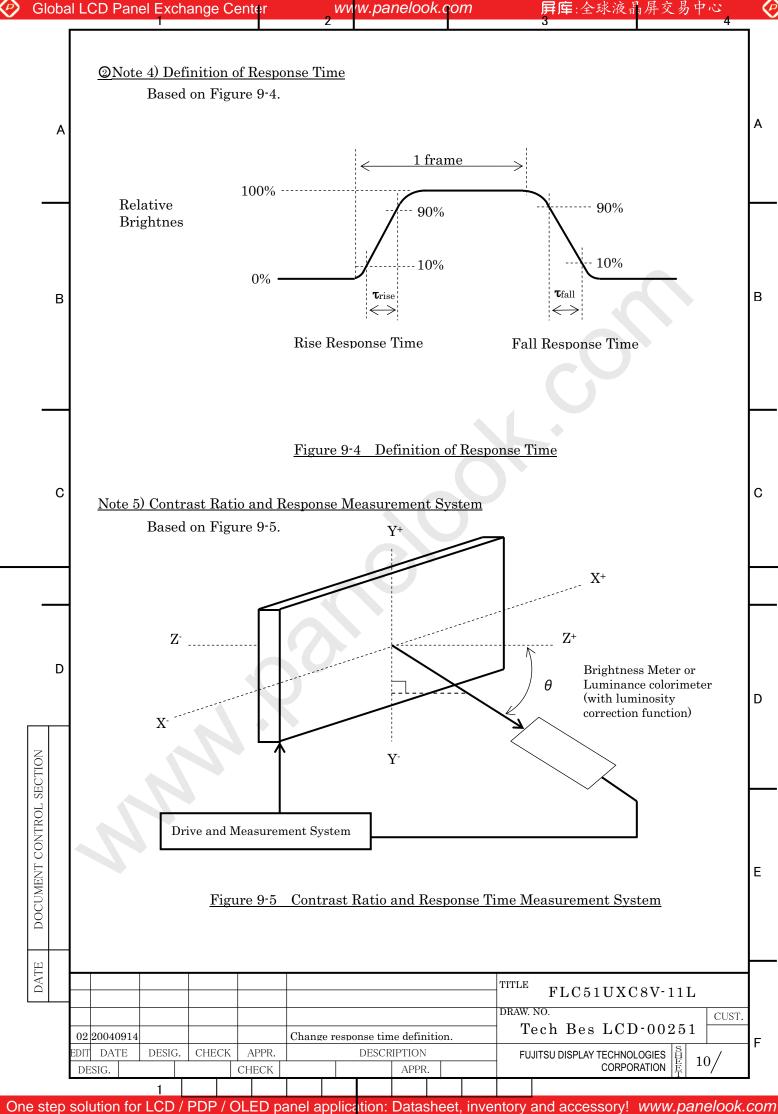
(\*1) Value at  $15\sim20$  minutes after lighting on.

(Note) •CS-1000 (MINOLTA Co. Ltd.) Field= $1^{\circ}$  , L=500mm

- Back-light current = 6mA, Dark room condition (1 lux or less)
- •Be careful that the luminance meter, which you use, may not be able to get correct brightness If it's no set correctly.

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#### 10. INTERFACE SPECIFICATIONS

#### 10-1 Signal descriptions

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Table 10-1 shows the description and configuration of interface signals (CN1).

Table 10-1 Interface signals (CN1)

Pin No.	Symbol	I/O	Function
1	RxO0-	I	Negative differential input
2	RxO0+	I	Positive differential input
3	RxO1-	I	Negative differential input
4	RxO1+	I	Positive differential input
5	RxO2-	I	Negative differential input
6	RxO2+	I	Positive differential input
7	GND	_	G round
8	RxOC-	I	Negative differential input
9	RxOC+	I	Positive differential input
10	RxO3-	I	Negative differential input
11	RxO3+	I	Positive differential input
12	RxE0-	I	Negative differential input
13	RxE0+	I	Positive differential input
14	GND	_	G round
15	RxE1-	I	Negative differential input
16	RxE1+	I	Positive differential input
17	GND	+	G round
18	RxE2-	I	Negative differential input
19	RxE2+	I	Positive differential input
20	RxEC-	I	Negative differential input
21	RxEC+	I	Positive differential input
22	RxE3-	I	Negative differential input
23	RxE3+	I	Positive differential input
24	GND	_	G round
25	SELL LVDS	I	Select LVDS data order *1
26	PD	I	LVDS Core Power Down
27	HRCOFF	I	Fast response function control*2
28	Vcc	_	+12V power supply
29	Vcc	_	+12V power supply
30	Vcc	_	+12V power supply
: FI-X	30S-HF (Japan	Aviatio	n Electronics)

Connector User's connector: FI-X30M (Japan Aviation Electronics) FI-X30H, FI-X30C

- \*1: 3.3V CMOS Signal input. (High or Low)
- \*2: The fast response function is OFF at "H". Refer to item 10.4 for change over of timing.
- \*3: When using a connector other than the recommended one, a defect in the initial stage or a problem concerning long term reliability may occur.

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#### 10-2 LVDS Data Assignment

Table 10-2 shows the LVDS Data Assignment.

Α													, I A
	Inp	ut signal	*1		ansmitter CF383,C385	Interfa	ce conr	nector		Receiver S90CF386		input LVDS)	$\prod^{n}$
	SEL LVDS	Low	High	pin	INPUT	System side	LC pin	D module	pin	OUTPUT	Low	High	
		RO2	RO0	51	TxIN0				27	RxOUT0	RO2	RO0	Ì∟
		RO3 RO4	RO1 RO2	52 54	TxIN1 TxIN2	Tx OUT0+	2	RxO0+	29 30	RxOUT1 RxOUT2	RO3 RO4	RO1 RO2	
		RO5	RO3	55	TxIN3				32	RxOUT3	RO5	RO3	
		RO6	RO4	56	TxIN4	Tx OUT0-	1	RxO0-	33	RxOUT4	RO6	RO4	11
		RO7	RO5	3	TxIN6				35	RxOUT6	RO7	RO5	
		GO2 GO3	GO0 GO1	4 6	TxIN7 TxIN8				37 38	RxOUT7 RxOUT8	GO2 GO3	GO0 GO1	11
٦١		GO4	GO2	7	TxIN9	Tx OUT1+	4	RxO1+	39	RxOUT9	GO4	GO2	Пь
В		GO5	GO3	11	TxIN12	12 0011+	4	IXO1+	43	RxOUT12	GO5	GO3	В
		GO6	GO4	12	TxIN13				45	RxOUT13	GO6	GO4	11
		GO7 BO2	GO5 BO0	14 15	TxIN14 TxIN15	Tx OUT1-	3	RxO1-	46 47	RxOUT14 RxOUT15	GO7 BO2	GO5 BO0	
		BO2 BO3	BO1	19	TxIN13				51	RxOUT18	BO2 BO3	BO1	11
	LVDS	BO4	BO2	20	TxIN19				53	RxOUT19	BO4	BO2	
	Odd	BO5	BO3	22	TxIN20	Tx OUT2+	6	RxO2+	54	RxOUT20	BO5	BO3	
		BO6	BO4	23	TxIN21				55	RxOUT21	BO6	BO4	11
		BO7 RSVD	BO5 RSVD	24 27	TxIN22 TxIN24		_	(	1 3	RxOUT22 RxOUT24	BO7 Not use	BO5 Not use	ıL
		RSVD	RSVD	28	TxIN25	Tx OUT2-	5	RxO2-	5	RxOUT25	Not use	Not use	11
		ENAB	ENAB	30	TxIN26				6	RxOUT26	ENAB	ENAB	11
		RO0	RO6	50	TxIN27				7	RxOUT27	RO0	RO6	11
		RO1	RO7	2	TxIN5	Tx OUT3+	11	RxO3+	34	RxOUT5	RO1	RO7	11
		GO0 GO1	GO6 GO7	8 10	TxIN10 TxIN11				41 42	RxOUT1 RxOUT11	GO0 GO1	GO6 GO7	11
C		BO0	BO6	16	TxIN16	T. OLITO	10	D 00	49	RxOUT16	BO0	BO6	IIс
		BO1	BO7	18	TxIN17	Tx OUT3-	10	RxO3-	50	RxOUT17	BO1	BO7	11
		RSVD	RSVD	25	TxIN23				2	RxOUT23	Not use	Not use	
		DCLK		31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	9	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	DCLK		
		RE2	RE0	51	TxIN0				27	RxOUT0	RE2	RE0	i 📙
		RE3	RE1	52	TxIN1	Tx OUT0+	13	RxE0+	29	RxOUT1	RE3	RE1	
		RE4	RE2 RE3	54	TxIN2 TxIN3				30 32	RxOUT2 RxOUT3	RE4 RE5	RE2 RE3	
$\neg$		RE5 RE6	RE3	55 56	TxIN3 TxIN4		4.0	<b>5 5</b>	33	RXOUT4	RE6	RE3 RE4	
		RE7	RE5	3	TxIN6	Tx OUT0-	12	RxE0-	35	RxOUT6	RE7	RE5	11
		GE2	GE0	4	TxIN7				37	RxOUT7	GE2	GE0	11
		GE3	GE1	6	TxIN8				38	RxOUT8	GE3	GE1	11
D		GE4 GE5	GE2 GE3	7 11	TxIN9 TxIN12	Tx OUT1+	16	RxE1+	39 43	RxOUT9 RxOUT12	GE4 GE5	GE2 GE3	11
		GE6	GE3	12	TxIN13				45	RxOUT13	GE6	GE4	
		GE7	GE5	14	TxIN14	Tx OUT1-	15	RxE1-	46	RxOUT14	GE7	GE5	ΙID
		BE2	BE0	15	TxIN15	1x 0011-	15	KXE1-	47	RxOUT15	BE2	BE0	
+	LVDS	BE3 BE4	BE1 BE2	19 20	TxIN18 TxIN19				51 53	RxOUT18 RxOUT19	BE3 BE4	BE1 BE2	
	Even	BE5	BE3	22	TxIN20			B 70	54	RxOUT20	BE5	BE3	11
	Even	BE6	BE4	23	TxIN21	Tx OUT2+	19	RxE2+	55	RxOUT21	BE6	BE4	11
		BE7	BE5	24	TxIN22				1	RxOUT22	BE7	BE5	11
		RSVD	RSVD	27	TxIN24	Tx OUT2-	18	RxE2-	3	RxOUT24	Not use	Not use	l I
		RSVD RSVD	RSVD RSVD	28 30	TxIN25 TxIN26				5 6	RxOUT25 RxOUT26	Not use Not use	Not use Not use	
		RE0	RE6	50	TxIN27				7	RxOUT27	RE0	RE6	
		RE1	RE7	2	TxIN5	T OLITO	00	D E0	34	RxOUT5	RE1	RE7	11
		GE0	GE6	8	TxIN10	Tx OUT3+	23	RxE3+	41	RxOUT10	GE0	GE6	H
		GE1 BE0	GE7 BE6	10 16	TxIN11 TxIN16				42 49	RxOUT11 RxOUT16	GE1 BE0	GE7 BE6	H
		BE1	BE7	18	TxIN16	Tx OUT3-	22	RxE3-	49 50	RXOUT16 RXOUT17	BE1	BE7	E
		RSVD		25	TxIN23				2	RxOUT23	Not use	Not use	
		DCLK		31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	21 20	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	Not use		
$\coprod$	*	1 RSV	D (rese	rved)	pin on a t	ransmitter sl	nould	be connecte	d with	Ground.			

		-			1 . 1 1.	.1 1.		• . •	C.I. I.OD				
		• Ir	iput odd	or even	data depending o	on the dis	play po	sition (	of the LCD mo	dule.			i
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#### 10-3 Color Data Assignment

Table 10-3 shows the Color Data Assignment.

Table 10-3 Color Data Assignment

Co	Odd Even				R I	npı	ıt d	ata	a			(	G Iı	npu	ıt d	lata	ì			]	B Iı	npı	ıt d	lata	3	
	Odd		R7	R6	R5	R4	R3	R2	2 R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	<b>B5</b>	B4	В3	<b>B2</b>	B1	В0
	Even		R7	R6	R5	R4	R3	R2	<b>R</b> 1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	<b>B6</b>	<b>B5</b>	<b>B4</b>	В3	<b>B2</b>	B1	В0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Or	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
obr	Green		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
C	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Sic	Red		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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Red		:	:	:	:	:	:	:	:	:	:	:	:	:			:	:	<b>:</b>	:	:	:	:	:	:	:
	<u>+</u>	:	:	:	:	:	:	:	:	:	:	:	:	:					:	:	:	:	:	:	:	:
	Brighter	253	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	254	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
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9	Brighter	253	0				•	•	0	0	1	1	1	1	1	1		1	0	•	•	•			0	0
	Di ignitei ↓	254	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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	Brighter	253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Û	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note.1) Definition of gray scale: Color (n)  $\cdots$  "n" indicates gray scale level.

Larger number means brighter level.

Note.2) Data; 1:High, 0:Low

Note 3) Color data consist of 8 bit red, green and blue data of odd and even number pixel data. Total data number is 48 signals. This module is able to display 16,777,216 colors because each red, green and blue data is controlled independently.

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#### 10-4 Input Signal Timing

Table 10-4 and Figure 10-1 shows the Input Signal Timing at LVDS transmitter.

Table 10-4 Timing Characteristics

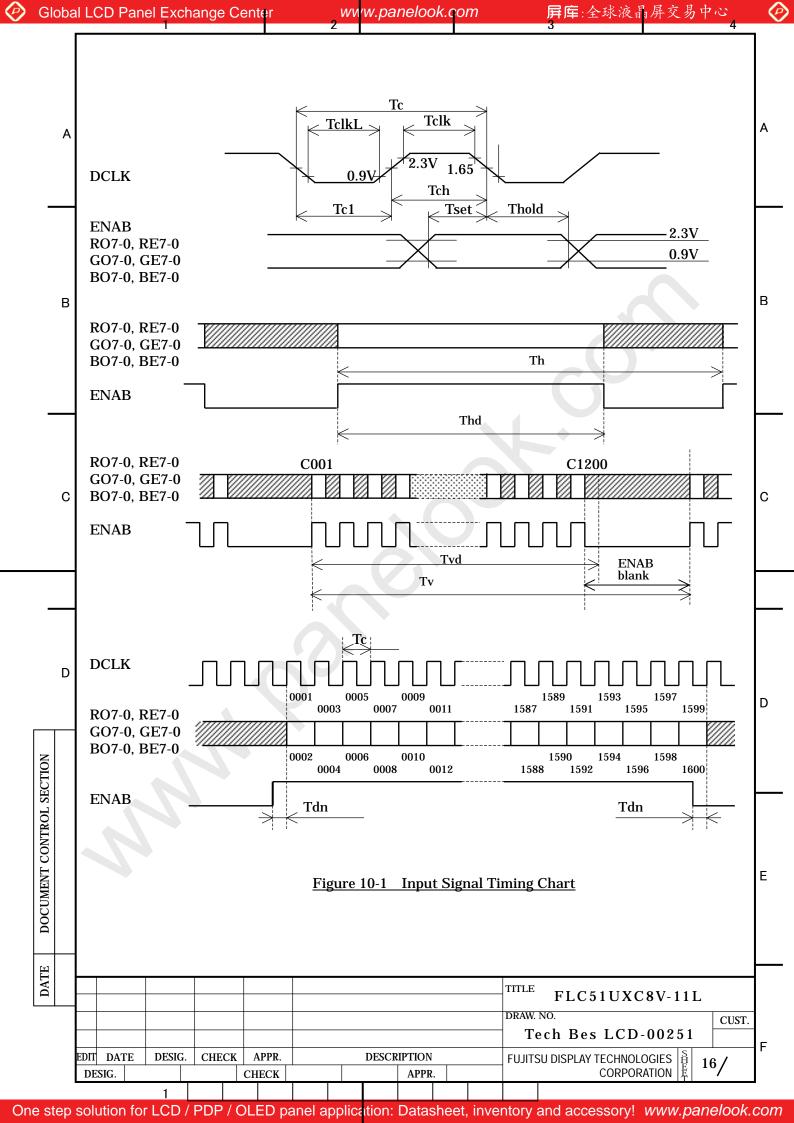
 $(Ta=0~45^{\circ}C, Vcc=12\pm0.5V)$ 

	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLK	Period Frequency	Tc 1/Tc	11.765 50.000	12.345 81.000	20.000 85.000	ns MHz	
signal	Duty	Tch/Tc	45	50	55	%	
(Clock)	High time	TclkH	3.5		_	ns	
	Low time	TclkL	3.5		_	ns	
DCLK-Data	Setup time	Tset	3		_	ns	
Timing	Hold time	Thold	2		_	ns	
	Horizontal Period	Th	865*1	1080	1130*1	DCLK	
	Hor. Period	Th	13.0	13.3	14.65	μs	
	Hor. Display period	Thd	800	800	800	DCLK	*2
	Vertical Period					Th	
	Ver. Frequency	Tv	$1207^{*1}$	1250	1280*1	Hz	
ENAB		1/Tv	50	60	62	Hz	
signal		Tvd	1200	1200	1200	Hz	
	Ver. Display period					Th	
	Data-ENAB timing	Tdn	_	0	_	DCLK	*3
	Vertical ENAB blank.	ENAB blank	5500			DCLK	*4

\*1) • horizontal display position is specified by the rise of ENAB.

The data latched at falling edge of DCLK after rise of ENAB is displayed at the left edge of the display area.

- •Vertical display position is specified by the rise of ENAB after low level continuation over 5500 DCLK. The data latched at the rise of ENAB is displayed at the top line of the display area.
- \*2) If the "High" level period of ENAB is less than 800 DCLK, black color is displayed at the rest of the display area.
- \*3) If ENAB does not synchronize with the effective display data, the display position does not fit to the display area.
- \*4) If Vertical ENAB blank time shorter than 5500DCLK, LCD unit may stop display. Please keep ENAB blank at anytime. (ex.: Change display mode.)



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#### 11. BACK-LIGHT SPECIFICATION

Global LCD Panel Exchange Center

#### 11-1 Pin configuration for Back-light

Table 11-1 shows the description and Pin assignment of the connectors (CN-A to D) for the Back-light of this LCD module.

Table 11-1 Pin Assignment of CN-A to CN-D

No.	Pin No.	Symbol	Function	Cable Color
	1	Vн1	Power supply (High voltage)	Pink
CN-A	2	V <sub>H2</sub>	Power supply (High voltage)	Orange
	3	-	NC	
	4	$V_{L^1}$	Power supply (Low voltage)	Blue
	5	$V_{L^2}$	Power supply (Low voltage)	Gray
CN-B	1	V <sub>H3</sub>	Power supply (High voltage)	Red
CIV-D	2	$V_{L3}$	Power supply (Low voltage)	White
	1	$V_{H4}$	Power supply (High voltage)	Pink
	2	V <sub>H5</sub>	Power supply (High voltage)	Orange
CN-C	3	-	NC	
	4	$V_{\rm L4}$	Power supply (Low voltage)	Blue
	5	$V_{\scriptscriptstyle L5}$	Power supply (Low voltage)	Gray
CN-D	1	V <sub>H6</sub>	Power supply (High voltage)	Red
CIV-D	2	$V_{L6}$	Power supply (Low voltage)	White

Housing(CN-A, CN-C):BHR-05VS-1 Connector (CN-B, CN-D):BHSR-02VS-1

SBH-001T-P0.5

Contact SBHS-002T-P0.5

SM04(9-E2)B-BHS-1

User's Connector: Post with base: SM02B-BHSS-1-TB

**Supplier** Japan Solder less Terminal manufacturing Company LTD. (J.S.T.)

11-2 CCFL

Supplier: SANKEN ELECTRONICS Co.LTD. Part No.: SS26E4175E8550C2882710S

#### 11-3 Life

The life of the back-light is a minimum of 50,000 hours at the following conditions.

(1) Working conditions

①Ambient temperature :  $25\pm5$ °C ②Tube current (I<sub>L</sub>) : (6mA or less)

(2) Definition of life

①Brightness becomes 50% or less than the minimum brightness value shown in Table 9-1.

②Flashing.

#### 11-4 <u>Lamp assembly set (for replacement)</u>

Lamp assembly set (with charge) is prepared for replacing old lamp to new one.

This set consists of an upper lamp assembly and a lower lamp assembly.

Type number: FLCL-26

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#### 12. APPEARANCE SPECIFICATIONS

#### 12-1 Appearance

Global LCD Panel Exchange Center

12-1-1 Appearance specification

No.	Item		Judg	gment method an	d standard					
1	Bright spot (high a	nd Low)	<u>≤</u> 6 dots		(Note 1					
2	Bright spot connect (high and low)	tion	≤2 pair (Note 1							
3	Total of bright spot		<u>&lt;6</u> dots							
4	Dark spot		<u>≤</u> 10 dots		(Note 2					
5	Dark spot connection	on	<b>≤</b> 4pairs		(Note 2)					
6	Total of dark spot		<u>≤</u> 10 dots		(Note 2					
7	Total of dot defect (bright and dark)		<b>≤</b> 10 dots							
8	Distance of	high-hgh	≥15mm							
	bright spot	others	<u>≥</u> 5mm							
9	Distance of dark sp	ot	<u>≥</u> 5mm							
10	Scratch on polarize				Ignore					
	line shape			L≤6	Ignore					
			0.03 <w<u>≤0.05</w<u>	6 <l<u>≤12</l<u>	<u>≤</u> 7					
				12 <l< td=""><td>0</td></l<>	0					
				L <u>≤</u> 0.6	Ignore					
			0.05 <w<u>≤0.10</w<u>	0.6 <l<u>≤5</l<u>	<u>≤</u> 5					
				5 <l< td=""><td>0</td></l<>	0					
			0.10 <w< td=""><td></td><td>0</td></w<>		0					
11	Dent on polarizer,			D≤0.3	Ignore					
	dot shape		0.3<]	D <u>≤</u> 0.4	<u>≤</u> 9					
	_		0.4<]	0						
12	Dubble to malant			D <u>≤</u> 0.3						
	Bubble in polarizer		0.3<]	<u>≤</u> 4						
			0.5<]	0						
10	Dla alı suktta sasıtı		D≦	Ignore						
13	Black white spot (Foreign circular m	attori	0.15<	<u>≤</u> 5						
	(For eight circular in	atter)	0.5<]	0						
14	Light leakage by fo	reign	]	D <u>≤</u> 0.3	Ignore					
	articles				<u>≤</u> 5					
	<b>▲</b> •		0.6<]		0					
15	Lints,		W <u>≤</u> 0.03		Ignore					
13	black/white line		_	L≤6	Ignore					
	DIGGIV WITHE IIIIE		0.03 <w<u>≤0.05</w<u>	6 <l<u>≤12</l<u>	<u>≤</u> 6					
				12 <l< td=""><td>0</td></l<>	0					
				L≤0.6	Ignore					
			0.05 <w<u>≤0.10</w<u>	0.6 <l<u>≤5</l<u>	<u>≤</u> 3					
				5 <l< td=""><td>0</td></l<>	0					
			0.10 <w< td=""><td>Conform to No.13</td></w<>	Conform to No.13						
16	Mura			6% ND filter from: n: Black, White,	m center of display.					

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#### 13. ENVIRONMENTAL SPECIFICATIONS

Table 13-1 show the environmental specifications.

Table 13-1 Environmental specifications

Global LCD Panel Exchange Center

Item		Condition	Remark				
T	Operation	0~45°C	Temperature on surface of LCD				
Temperature	Storage	-20~60°C	panel should be under $54^{\circ}$ C.				
11	Operation	20~85 % RH	Maximum wet-bulb temperature				
Humidity	Storage	5~85%RH	should not exceed 29°C. No condensation.	$\ $ <sub>B</sub>			
Vibration	Non-operation	10~500Hz, 1octave/20minute, 2G, 1.5mm max, 1hour each X, Y and Z directions	For single module without package.				
Shock	Non-operation	30G, 6ms, 1time each ±X, ±Y and ±Z directions.	without package.				

NOTE: Table 13-2 and Figure 13-1 show the shock resistance standard when module is packaged.

Table 13-2 Shock resistance standard when module is packaged

Dropping location	Dropping height	Count
A~J	60cm	1 time

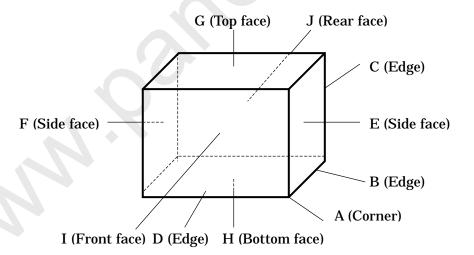
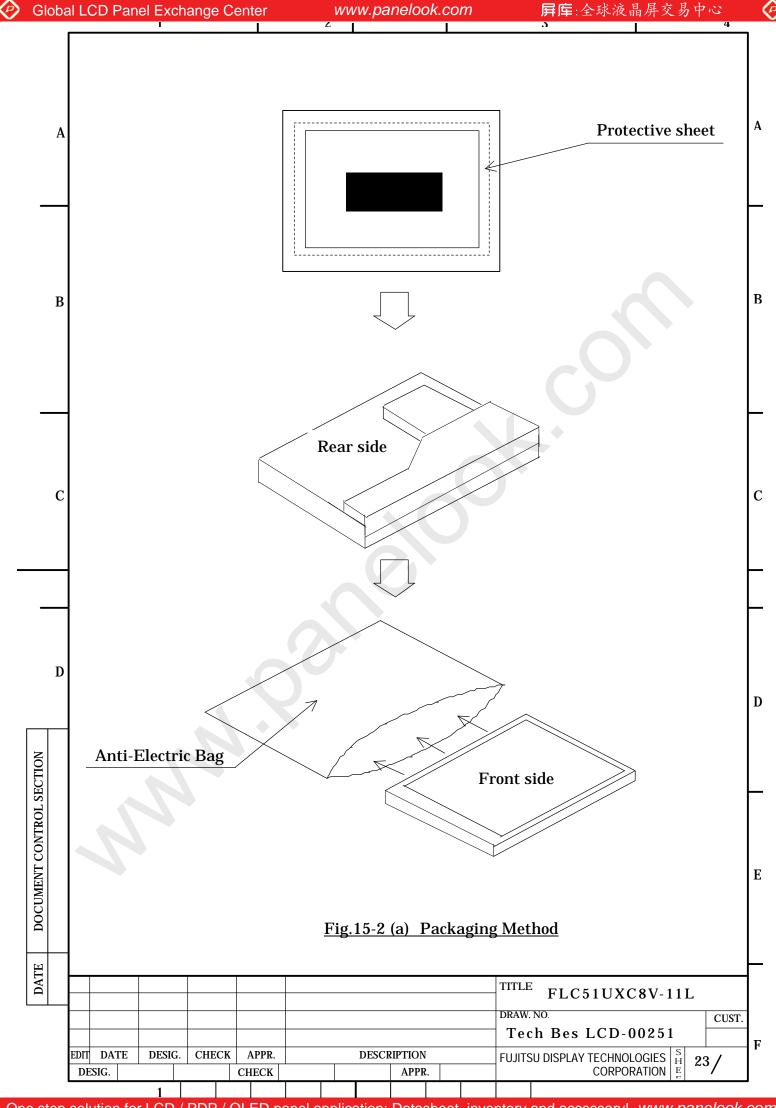
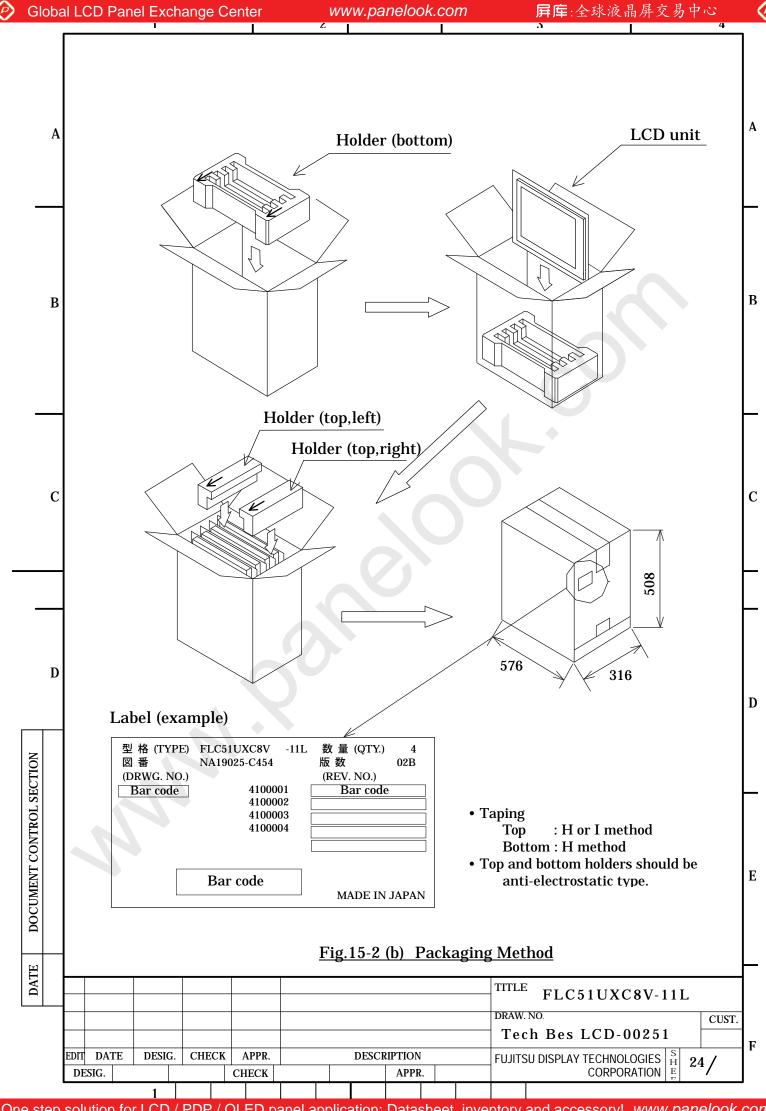
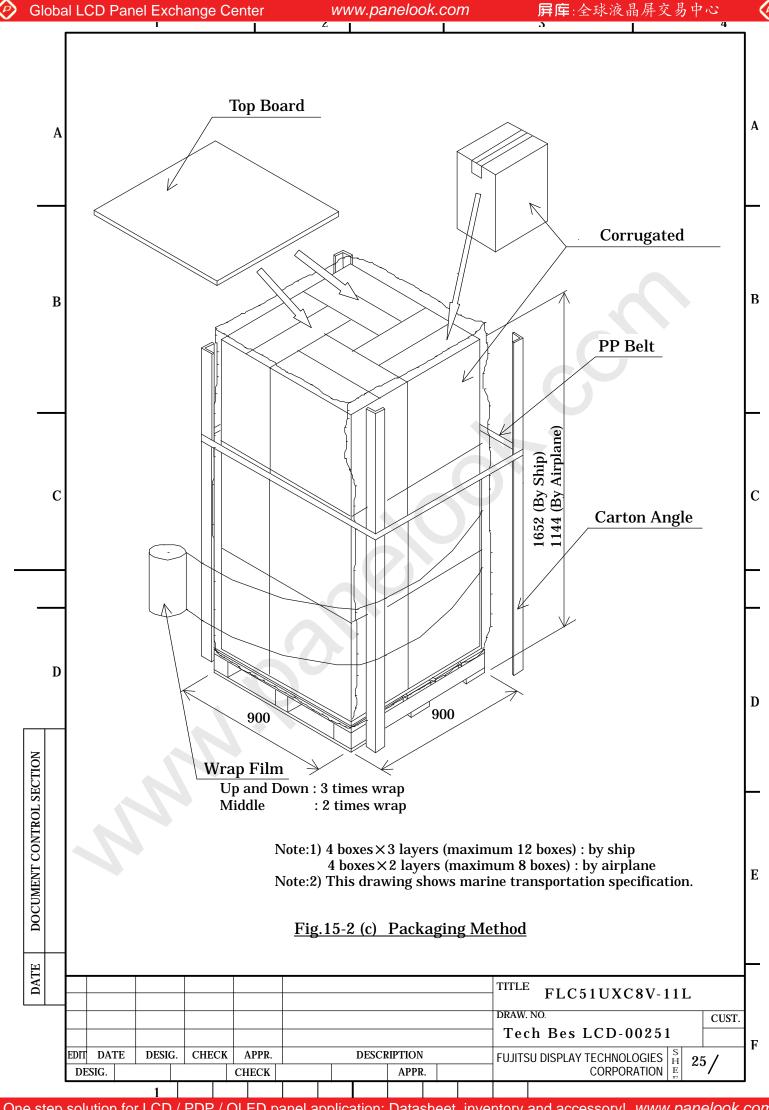
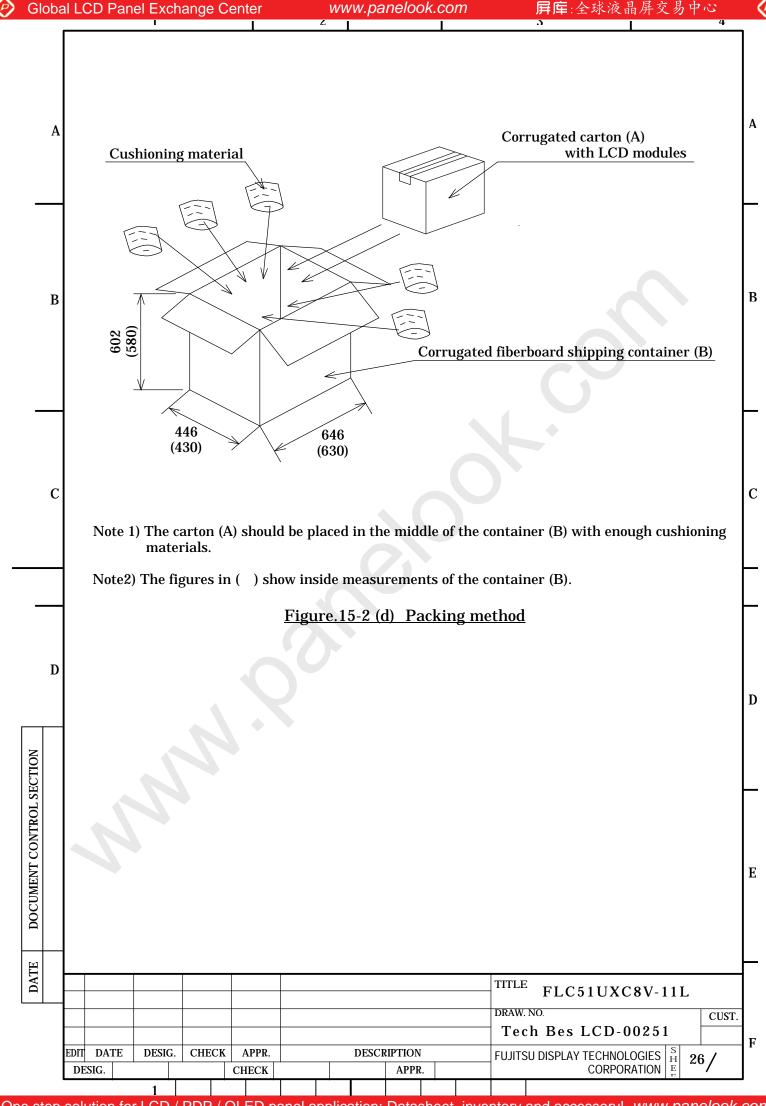


Figure 13-1 Direction to apply shock to package









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# 16.WARRANTY 17.PRECAUTIONS

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The warranty period is one year after shipping. Products which fail during this period are repaired or replaced without charge, unless the failure is caused by user.

Adhere to the following precautions to use this LCD module properly.

- (1) Handling of LCD panel
  - ① Do not apply any strong mechanical shock to the LCD panel. Since the LCD panel is made of glass, excessive shock may damage the panel or cause a malfunction.
  - 2 Do not press hard on the LCD panel surface.

In the LCD panel, the gap between two glass plates is kept perfectly even to maintain display properties and reliability. The hard pressure on the LCD panel may cause the following problems. If the pressure is over 2kg/cm2, the problem don't return to normal

- **1** Ununiformity of color
- 2 Disorder of orientation of liquid crystal

Problem (1) returns to normal condition after a while. Problem (2) returns to normal condition by turning the power off and turning on again.

However these operations should be avoided to insure reliability.

- 3 Do not scratch the polarizer film on the LCD panel surface.
  - •Do not press or rub the display surface with a hard tool, tweezers, etc.
  - For handling, use cotton or conductive gloves so that the display surface is not soiled.
  - ·If dust or dirt soils the display surface, clean it as follows with a soft cloth (deerskin, etc.)

[Dust] Wipe off with a soft cloth. (do not rub.)

[Dirt ] Apply clear water to a soft cloth and squeeze hard out of water drops, then lightly wipe off the specified parts. Only if the dirt is hardly wiped off, use isopropyl alcohol

Be careful not to splash the water or the solvents on the edge of polarizer and in the LCD unit.

The polarizer possibly exfoliates due to the solvent and water penetrated between the polarizer and the LCD panel.

Do not use unspecified solvent such as ketone (acetone, etc.) and aromatics (xylene, toluene, etc.)

(Caution) Be careful not to allow the water or solvent to enter the module.

·If saliva or water drops are left for a long period of time, the part may become deformed or discolored.

Wipe off immediately in the same way as for dirt.

•Do not allow oil to adhere to the module since excessive oil is hard to clean

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(5) Storage method

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CHECK

Anti-electric bag

